



Date: 16th February 2025

NOTICE

List of Shortlisted Candidates for Stage – 3 Written Examination (Paper-II) for recruitment to the post of Junior Assistant, Pay Level 3 (7th CPC) against advertisement No. Advt No. Estt. /2023/Gr-C (JA)/2550 dated September 22, 2023

Based on the performance in the Stage-2 (Skill Test – Computer based Typing Test) held on February 16, 2025 for the post of Junior Assistant, the following candidates are shortlisted for Stage-3 written Examination (Paper-II) which will be held on February 17, 2025 at 9:00 A.M. in the Department of Electronics Engineering, SVNIT Surat. The duration of Stage-3 Examination will be of two hours.

| Sr. No. | Registration ID | Roll No. |
|---------|--------------------|----------|
| 1. | SVNIT2023/09/29570 | JA006 |
| 2. | SVNIT2023/09/29580 | JA014 |
| 3. | SVNIT2023/10/29926 | JA095 |
| 4. | SVNIT2023/10/30298 | JA175 |
| 5. | SVNIT2023/10/30570 | JA243 |
| 6. | SVNIT2023/10/30669 | JA279 |
| 7. | SVNIT2023/10/30789 | JA315 |
| 8. | SVNIT2023/10/30799 | JA321 |

The above shortlisted candidates are required to appear for Stage-3 Written Examination (Paper-II) which will be held in the Department of Electronics Engineering, SVNIT Surat as per schedule mentioned below:

Schedule of conduct of Stage-3 Written Examination (Paper-II)

| Serial No. | Particulars | Date / Time | Venue |
|------------|--|--|---|
| 1 | Stage-3 Written Examination (Paper-II) | Date - 17-02-2025 (Monday) Time - 9:00 A.M. to 11:00 A.M. | Department of Electronics Engineering, Sardar Vallabhbhai National Institute of Technology, Ichchhanath, Surat - 395007, Gujarat |

Note: Any application, even under the RTI Act, etc. with regard to the Written Examination will not be entertained till completion of entire recruitment process.

Sd/-

REGISTRAR